

# SILOS-X Code Coverage

## Introduction

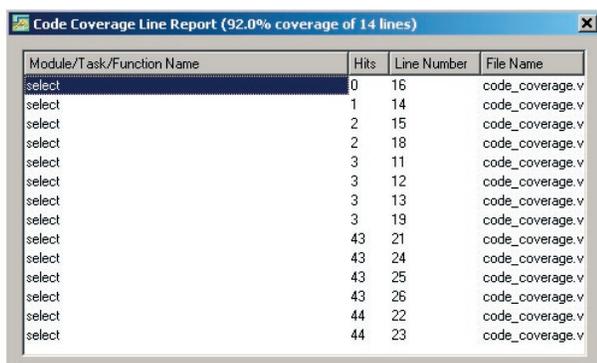
Code coverage analysis measures how thoroughly a simulation testbench exercises parts of a design. It provides an indication of the quality of the testbench: if some part of the design is not exercised by a testbench, then a test case may be missing from the test specification which was used to create the testbench. Alternatively, perhaps it is expected that the section is unexercised because the design is more general than strictly needed and some mode is unused. A section of code may also be unexercised because of a bug in the design.

There are various kinds of code coverage including statement coverage, path coverage, FSM coverage and expression coverage. SILOS-X code coverage reports behavioral lines that did not execute, and operators not fully exercised.

This application note describes how to create a line coverage report using SILOS-X.

Example Procedure for Using Line Coverage Feature of SILOS-X:

- The example project “code\_coverage.spj” will be used to explain the procedure.
- Start SILOS-X. and open the project “code\_coverage.spj” .



| Module/Task/Function Name | Hits | Line Number | File Name       |
|---------------------------|------|-------------|-----------------|
| select                    | 0    | 16          | code_coverage.v |
| select                    | 1    | 14          | code_coverage.v |
| select                    | 2    | 15          | code_coverage.v |
| select                    | 2    | 18          | code_coverage.v |
| select                    | 3    | 11          | code_coverage.v |
| select                    | 3    | 12          | code_coverage.v |
| select                    | 3    | 13          | code_coverage.v |
| select                    | 3    | 19          | code_coverage.v |
| select                    | 43   | 21          | code_coverage.v |
| select                    | 43   | 24          | code_coverage.v |
| select                    | 43   | 25          | code_coverage.v |
| select                    | 43   | 26          | code_coverage.v |
| select                    | 44   | 22          | code_coverage.v |
| select                    | 44   | 23          | code_coverage.v |

Figure 1. Code Coverage Line Report.

- To enable Code Coverage reporting, click on the Enable Code Coverage button if it is not already selected. If the Enable Code Coverage button is selected after inputting the files for the design, SILOS-X will display a message box to allow the user to reload the design and enable Code Coverage reporting.
- Click on the Go button to simulate the design.
- Select the Reports Code Coverage Line Report menu selection to open the Code Coverage Line Coverage report (Figure 1). This report is sorted by the number of times each line is executed (“Hits”) in ascending order. Normally you are interested in which lines are not executed (“0” Hits). However, the report can also be used to see which lines have a high number of executions, indicating a possible flaw in the design. The sort order can be changed by clicking on the desired sort column heading.
- While viewing the Code Coverage Line Report, double click on the first entry in the report that has zero “Hits”. This will open the file “code\_coverage.v”, and display the line that did not execute with a red icon in the left margin of the line.

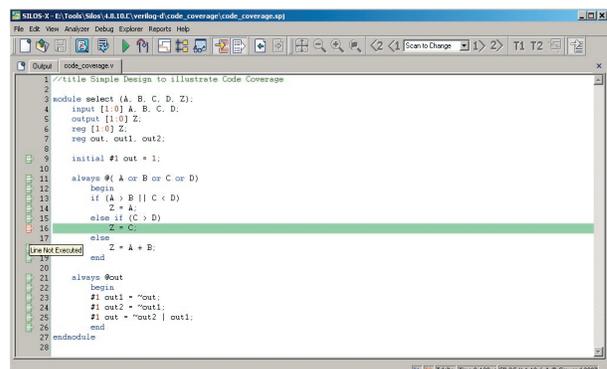


Figure 2. Data Tip Showing Code Coverage Information.

| Module/Task/Function Name | Hits | Line Number | File Name       |
|---------------------------|------|-------------|-----------------|
| select                    | 0    | 16          | code_coverage.v |
| select                    | 1    | 14          | code_coverage.v |
| select                    | 2    | 15          | code_coverage.v |
| select                    | 2    | 18          | code_coverage.v |
| select                    | 3    | 11          | code_coverage.v |
| select                    | 3    | 12          | code_coverage.v |
| select                    | 3    | 13          | code_coverage.v |
| select                    | 3    | 19          | code_coverage.v |
| select                    | 43   | 21          | code_coverage.v |
| select                    | 43   | 24          | code_coverage.v |
| select                    | 43   | 25          | code_coverage.v |
| select                    | 43   | 26          | code_coverage.v |
| select                    | 44   | 22          | code_coverage.v |
| select                    | 44   | 23          | code_coverage.v |

Figure 3. Code Coverage Line Report.

- Click the Main window to change the focus to the text window.
- Place the mouse cursor over the red icon in the left margin of the file “code\_coverage.v”, and a data tip will pop up stating “Line Not Executed” (Figure 2).
- A large number of hits in the Code Coverage Line Report (Figure 3) may indicate a looping problem in the design. To see an example of this, double click on the last entry in the Code Coverage Line Report to automatically go to that line of file “code\_coverage.v”. Notice that this line is in an “always” loop that triggers itself by recursively setting the variable in the sensitivity list. To see how many times the line was executed, change the focus back to the Main text window, place the mouse cursor over the green text box, and a data tip will pop up stating “Line Execution Count=44” (Figure 4).
- The Code Coverage results can be displayed in another program, such as Microsoft Excel. To demonstrate the exporting of the Code Coverage results, select the Reports Export menu selection to open the Export Code Coverage Data dialog box. Select a file name and click the OK button to close the dialog box. The file can be viewed in an editor window to verify the export format, or import the file into a program such as Microsoft Excel.

## Conclusion

The Code Coverage of a given test pattern can easily be measured using SILOS-X. Reports in various formats can be generated using this feature that list unexercised lines and hit count for the behavioral source code of a Verilog design.

```

1 //title Simple Design to illustrate Code Coverage
2
3 module select (A, B, C, D, Z);
4   input [1:0] A, B, C, D;
5   output [1:0] Z;
6   reg [1:0] Z;
7   reg out, out1, out2;
8
9   initial #1 out = 1;
10
11   always @(A or B or C or D)
12     begin
13       if (A > B || C < D)
14         Z = A;
15       else if (C > D)
16         Z = C;
17       else
18         Z = A + B;
19     end
20
21   always @out
22     begin
23       #1 out1 = out;
24       #1 out2 = out1;
25       LineExecutionCount = $out + "out2 | out1;
26     end
27 endmodule
28

```

Figure 4. Data Tip Showing Code Coverage Information.

## References

- [1] VIS Group (1996), .VIS: A system for Verification and Synthesis., in proceedings of 8th International Conference on Computer Aided Verification (CAV), pp428-432, Vol. 1102 Lecture Notes in Computer Science, Springer. July.
- [2] Expression Coverability Analysis: Improving code coverage with model checking Graeme D. Cunningham, Paul B. Jackson, Julian A.B. Dines
- [3] SILOS-X user manual